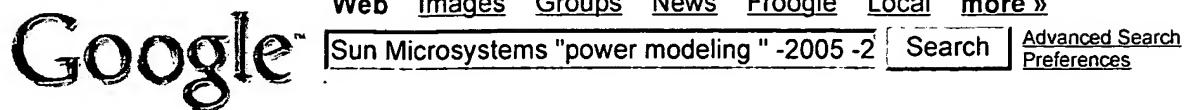


	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L13	17	IC same (power adj information)	USPAT	2005/07/14 14:14
2	BRS	L14	2	(power adj information) and (summary adj information)	USPAT	2005/07/14 14:18
3	BRS	L15	0	(power adj information) and (summary adj information) and ((integrated adj circuits) or ICs)	USPAT	2005/07/14 14:19
4	BRS	L16	0	(summary adj information) same power same circuits	USPAT	2005/07/14 14:19
5	BRS	L17	6	(summary adj information) same circuits	USPAT	2005/07/14 14:21
6	BRS	L18	5	(summary adj information) same circuits and power	USPAT	2005/07/14 14:21
7	BRS	L19	0	(summary adj information) same circuits same power	USPAT	2005/07/14 14:21
8	BRS	L20	0	(summary adj information) same (power adj information)	USPAT	2005/07/14 14:21
9	BRS	L21	8	(summary) same (power adj information)	USPAT	2005/07/14 14:57
10	BRS	L22	0	703/13/ccls.	USPAT	2005/07/14 14:57
11	BRS	L23	575	703/13.ccls.	USPAT	2005/07/14 14:57
12	BRS	L24	264	703/13.ccls. and power	USPAT	2005/07/14 14:59
13	BRS	L25	127	703/13.ccls. and power and voltage	USPAT	2005/07/14 14:59
14	BRS	L26	837	716/1.ccls.	USPAT	2005/07/14 15:07
15	BRS	L27	487	716/1.ccls. and power	USPAT	2005/07/14 15:07
16	BRS	L28	123	716/1.ccls. and power and modeling	USPAT	2005/07/14 15:07



Web Results 1 - 10 of about 45 for Sun Microsystems "power modeling" -2005 -2004 -2003 -2002 -2001 -2

Tip: Save time by hitting the return key instead of clicking on "search"

34th DAC FULL-DAY TUTORIAL

James Gateley of **Sun Microsystems** will explain how logic emulation systems have helped validate ... Low power designs require accurate **power modeling** ...

jamaica.ee.pitt.edu/Archives/NewsletterArchives/v27/n1/tutors.html - 20k - Cached - Similar pages

ICCAD '96

6C.3, A **POWER MODELING** AND CHARACTERIZATION METHOD FOR THE CMOS STANDARD CELL

LIBRARY ... Jean Brouwers - **Sun Microsystems**, Palo Alto, CA ...

jamaica.ee.pitt.edu/Archives/NewsletterArchives/v26/n2/Iccad.html - 78k - Cached - Similar pages

MDL Alumni.

... for Systems-on-a-Chip, currently at **Sun Microsystems**, San Jose, CA. 25) Yan Zhang, Ph.D. 8/99, On-Chip Interconnect Power Modeling and Optimization, ...

www.cse.psu.edu/~mdl/alumni.html - 9k - Cached - Similar pages

Table of Contents ICCAD 96

6C.3 A **Power Modeling** And Characterization Method For The CMOS Standard Cell

Library: J.-Y. ... Panel Members : Jean Brouwers, **Sun Microsystems**, Palo Alto, CA ...

www.sigda.org/Archives/ProceedingArchives/Iccad/Iccad96/papers/1996/iccad96/htmfiles/sun_sgi/iccadtoc.htm - 53k - Cached - Similar pages

SIGDA Super Compendium, DATE 1998, Table of Contents

Sridhar Narayanan, **Sun Microsystems**, USA Gert Jan van Rootselaar, Philips Research

Labs, ... Characterization-Free Behavioral **Power Modeling** [p. ...

www.sigda.org/Archives/ProceedingArchives/

Compendiums/papers/1998/date98/htmfiles/sun_sgi/datetoc.htm - 68k - Cached - Similar pages

[More results from www.sigda.org]

iccad, 1996 International Conference on Computer-Aided Design ...

A **Power Modeling** and Characterization Method for the CMOS Standard Cell

Library (Abstract) ... Panelists: Jean Brouwers, **Sun Microsystems**, Palo Alto, CA; ...

csdl.computer.org/comp/proceedings/iccad/1996/7597/00/7597toc.htm - Similar pages

Table of Contents DATE 98

Sridhar Narayanan, **Sun Microsystems**, USA Gert Jan van Rootselaar, Philips Research

... Characterization-Free Behavioral **Power Modeling** [p 767]: A. Bogliolo, ...

ballade.cs.ucla.edu/~kohcc/sigdadm/ date98/papers/1998/date98/htmfiles/sun_sgi/frames/datetoc.htm - 63k - Cached - Similar pages

[PDF] Proceedings of the IEEE 1995 CUSTOM INTEGRATED CIRCUITS CONFERENCE ...

File Format: PDF/Adobe Acrobat

Brassington, **Sun Microsystems**, Inc., Menlo Park, CA ... A **Power Modeling** and Characterization Method for Logic. Simulation, H. Sarin and A. McNelly, ...

ieeexplore.ieee.org/iel3/ 3910/11340/00518123.pdf?arnumber=518123 - Similar pages

[PDF] Proceedings Design, Automation and Test in Europe - Proceedings ...

File Format: PDF/Adobe Acrobat

Sridhar Narayanan, **Sun Microsystems, USA**. Gert Jan van Rootselaar, Philips Research

Labs, ... Characterization-Free Behavioral Power Modeling ...

ieeexplore.ieee.org/iel4/ 5270/14300/00655828.pdf?arnumber=655828 - [Similar pages](#)

[[More results from ieeexplore.ieee.org](#)]

Design, Automation and Test in Europe

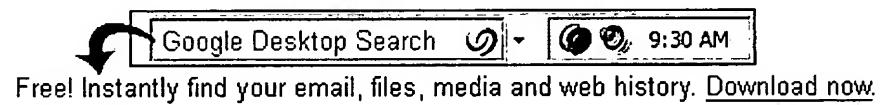
Sridhar Narayanan, **Sun Microsystems, USA** Gert Jan van Rootselaar, ... We propose

a new approach to RT-level power modeling for combinational macros, ...

www.date-conference.com/conference/1998/abstracts.htm - 255k - [Cached](#) - [Similar pages](#)

Google ►

Result Page: [1](#) [2](#) [Next](#)



[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#)

[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)

©2005 Google